

TRANSLATION

PATENT COOPERATION TREATY

PCT

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

(Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference 2F04188-PCT	FOR FURTHER ACTION	See Form PCT/IPEA/416
International application No. PCT/JP2004/017784	International filing date (<i>day/month/year</i>) 30.11.2004	Priority date (<i>day/month/year</i>) 01.12.2003
International Patent Classification (IPC) or national classification and IPC H04B1/30, H04L27/38, H04L27/22		
Applicant MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.		

1. This report is the international preliminary examination report, established by this International Preliminary Examining Authority under Article 35 and transmitted to the applicant according to Article 36.
2. This REPORT consists of a total of <u>4</u> sheets, including this cover sheet.
3. This report is also accompanied by ANNEXES, comprising: a. <input checked="" type="checkbox"/> (<i>sent to the applicant and to the International Bureau</i>) a total of <u>10</u> sheets, as follows: <input checked="" type="checkbox"/> sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications authorized by this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions). <input type="checkbox"/> sheets which supersede earlier sheets, but which this Authority considers contain an amendment that goes beyond the disclosure in the international application as filed, as indicated in item 4 of Box No. I and the Supplemental Box. b. <input type="checkbox"/> (<i>sent to the International Bureau only</i>) a total of (indicate type and number of electronic carrier(s)) _____, containing a sequence listing and/or tables related thereto, in computer readable form only, as indicated in the Supplemental Box Relating to Sequence Listing (see Section 802 of the Administrative Instructions).
4. This report contains indications relating to the following items: <input checked="" type="checkbox"/> Box No. I Basis of the report <input type="checkbox"/> Box No. II Priority <input type="checkbox"/> Box No. III Non-establishment of opinion with regard to novelty, inventive step and industrial applicability <input type="checkbox"/> Box No. IV Lack of unity of invention <input checked="" type="checkbox"/> Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement <input type="checkbox"/> Box No. VI Certain documents cited <input type="checkbox"/> Box No. VII Certain defects in the international application <input type="checkbox"/> Box No. VIII Certain observations on the international application

Date of submission of the demand	Date of completion of this report
Name and mailing address of the IPEA/JP	Authorized officer
Facsimile No.	Telephone No.

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Box No. I Basis of the report

1. With regard to the **language**, this report is based on the international application in the language in which it was filed, unless otherwise indicated under this item.
- ☐ This report is based on translations from the original language into the following language _____, which is the language of a translation furnished for the purposes of:
- ☐ international search (Rule 12.3 and 23.1(b))
- ☐ publication of the international application (Rule 12.4)
- ☐ international preliminary examination (Rule 55.2 and/or 55.3)
2. With regard to the **elements** of the international application, this report is based on *(replacement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to this report)*:
- ☐ the international application as originally filed/furnished
- ☒ the description:
- pages 1-5, 8-10, 12-15, 17-24, 26-31 as originally filed/furnished
- pages* 6, 7, 11, 16, 25 received by this Authority on 03.10.2005
- pages* _____ received by this Authority on _____
- ☒ the claims:
- nos. _____ as originally filed/furnished
- nos.* _____ as amended (together with any statement) under Article 19
- nos.* 22-30 received by this Authority on 20.02.2006
- nos.* _____ received by this Authority on _____
- ☒ the drawings:
- sheets fig. 1-11 as originally filed/furnished
- sheets* _____ received by this Authority on _____
- sheets* _____ received by this Authority on _____
- ☐ a sequence listing and/or any related table(s) – see Supplemental Box Relating to Sequence Listing.
3. ☒ The amendments have resulted in the cancellation of:
- ☐ the description, pages _____
- ☒ the claims, nos. 1-21
- ☐ the drawings, sheets/figs _____
- ☐ the sequence listing (*specify*): _____
- ☐ any table(s) related to sequence listing (*specify*): _____
4. ☐ This report has been established as if (some of) the amendments annexed to this report and listed below had not been made, since they have been considered to go beyond the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)).
- ☐ the description, pages _____
- ☐ the claims, nos. _____
- ☐ the drawings, sheets/figs _____
- ☐ the sequence listing (*specify*): _____
- ☐ any table(s) related to sequence listing (*specify*): _____

* If item 4 applies, some or all of those sheets may be marked "superseded."

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Box No. V	Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement		
1. Statement			
Novelty (N)	Claims	<u>22-30</u>	YES
	Claims		NO
Inventive step (IS)	Claims	<u>22-30</u>	YES
	Claims		NO
Industrial applicability (IA)	Claims	<u>22-30</u>	YES
	Claims		NO
2. Citations and explanations (Rule 70.7)			
<p>Document 1 (JP 2003-298451 A (Takuro SATO), 17 October 2003, paragraph [0002]) indicates that "[b]asically, the DC offset is generated in the mixer circuit. The mixer circuit mixes (multiplies) the signals from the RF signal source and the LO signal source (i.e. the local oscillator) and carries out frequency conversion. Therein, if LO signals enter a more roundabout path via the transmission system (i.e. input terminal on the RF signal-side of the mixer), then the LO signals (which have the same frequency as the reception frequency) will mix (of their own accord) so as to generate a DC offset."</p> <p>Document 2 (JP 2001-211098 A (Hitachi, Ltd.), 03 August 2001, paragraph [0030] & US 6826388 B & EP 1102413 A3) indicates that "switches (1401 and 1402) are inserted between the capacitor (1403) and the resistors (1404 and 1405) of the filter (140) in order to reduce the time constant during DC offset cancellation. As a result, it is possible to reduce the propagation delay of the filter (140), and thus the DC offset can be canceled more quickly without using the input short-circuiting switch (801) that is illustrated in FIG. 8."</p>			

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Box No. V

Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability;
citations and explanations supporting such statement

Claims 22 to 30

The feature whereby the "operations of the first amplification means are stopped during the correction process in the preceding stage, whereafter the first amplification means is operated subsequent to the completion of the correction process in the preceding stage but prior to the commencement of the correction process in the subsequent stage" is not disclosed or suggested in documents 1 to 2.